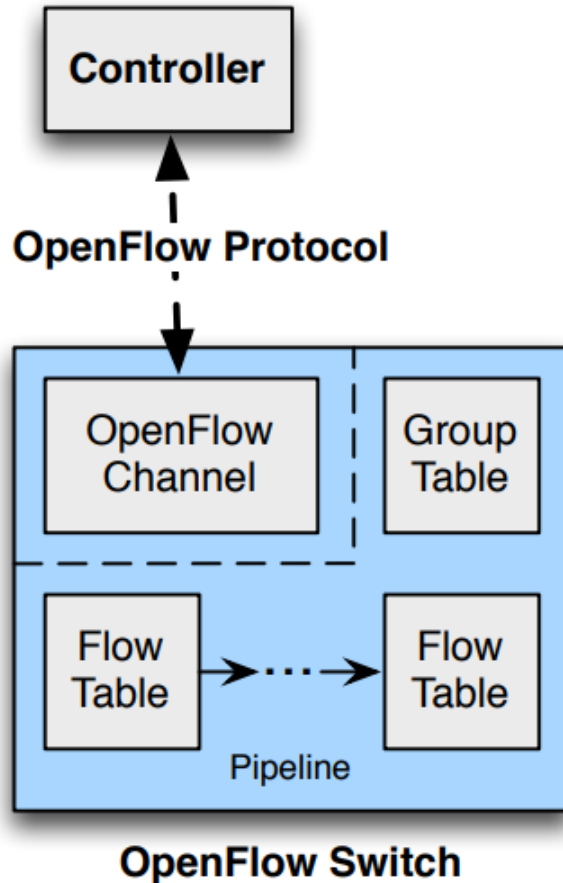


Exhibit 13

Illustrative Claim Chart for U.S. Patent No. 9,900,249

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches
[Preamble] A packet forwarding node, comprising:	<p>The Exemplary Dell Switches are packet forwarding nodes.</p> <p><i>See, e.g.,</i></p>  <p>Dell Networking S4810 High-performance 10/40GbE switch</p> <p>High-density, 1RU 48-port 10GbE switch with four 40GbE uplinks and ultra-low-latency, non-blocking performance to ensure line-rate performance; complete with feature-rich Dell Networking OS and storage optimization for iSCSI, FCoE transit and DCB.</p> <p>https://i.dell.com/sites/csdocuments/Shared-Content_data-Sheets_Documents/en/Dell_Networking_S4810_Spec_Sheet.pdf</p> <p>Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches support the OpenFlow specification.</p>

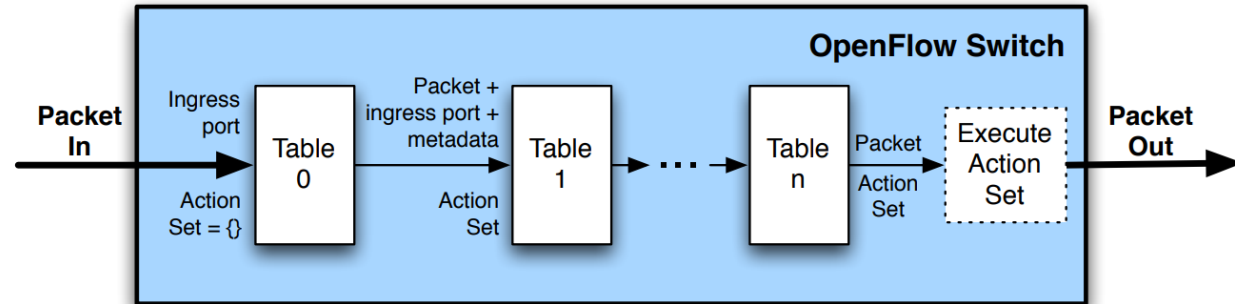
Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches
	<h2 data-bbox="611 277 1125 334">OpenFlow 1.3 Support</h2> <p data-bbox="611 358 1856 410">OpenFlow (OF) 1.3 [STD-1] is supported on the S3048-ON, S3100 series, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches.</p> <p data-bbox="611 427 1803 511">Dell Networking OS supports OpenFlow 1.3 message types. Although OpenFlow 1.3 is enabled, the OpenFlow 1.0 functionality is also supported. Additionally, the group flow and multipart message types features are supported. The multipart message types features replaces the statistics feature in OpenFlow 1.0 version.</p> <p data-bbox="596 527 1285 565">Dell OpenFlow Deployment and User Guide 4.0, p. 8.</p>

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches
	 <p>The diagram illustrates the main components of an OpenFlow switch. At the top, a Controller box is connected to a larger box representing the OpenFlow Switch. The connection is labeled OpenFlow Protocol and consists of a dashed line with arrows at both ends. Inside the OpenFlow Switch box, there is an OpenFlow Channel box in the top left. To its right is a Group Table box. Below these, separated by a dashed line, is a Pipeline section containing two Flow Table boxes connected by a dashed arrow pointing from left to right. The entire switch box is labeled OpenFlow Switch at the bottom.</p> <p>Figure~1: Main components of an OpenFlow switch.</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 8</p>

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches				
	<p>An OpenFlow Switch consists of one or more <i>flow tables</i> and a <i>group table</i>, which perform packet lookups and forwarding, and an <i>OpenFlow channel</i> to an external controller (Figure 1). The switch communicates with the controller and the controller manages the switch via the OpenFlow protocol.</p>				
<p>[a] a storage unit including a non-transitory computer readable medium configured to store a packet forwarding table used for forwarding a packet based on an identifier which identifies an interface of a packet forwarding node; and</p>	<p>The Exemplary Dell Switches have a storage unit including a non-transitory computer readable medium configured to store a packet forwarding table used for forwarding a packet based on an identifier which identifies an interface of a packet forwarding node.</p> <p><i>See, e.g.,</i></p> <p>Specifications: S4810 high-performance 10/40GbE switch</p> <table border="1"> <thead> <tr> <th data-bbox="617 735 1230 776">Dell SKU description</th><th data-bbox="1255 735 1904 971"></th></tr> </thead> <tbody> <tr> <td data-bbox="617 776 1230 971"> S4810 S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, I/O Panel to PSU Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow, Rear Mnt Bracket S4810, 48x 10GbE SFP+, 4x QSFP+, 1x DC PSU, 2x Fans, I/O Panel to PSU Airflow </td><td data-bbox="1255 735 1904 971"> VRF-lite: 64 instances Line-rate layer 2 switching: All protocols, including IPv4 and IPv6 Line-rate layer 3 routing: IPv4 and IPv6 IPv4 host table size: 8K IPv6 host table size: 4K IPv4 multicast table size: 4K LAG load balancing: Based on Layer 2, IPv4 or IPv6 headers Latency: 800ns Packet buffer memory: 9MB CPU memory: 2GB </td></tr> </tbody> </table> <p>https://i.dell.com/sites/csdocuments/Shared-Content_data-Sheets_Documents/en/Dell_Networking_S4810_Spec_Sheet.pdf</p>	Dell SKU description		S4810 S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, I/O Panel to PSU Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow, Rear Mnt Bracket S4810, 48x 10GbE SFP+, 4x QSFP+, 1x DC PSU, 2x Fans, I/O Panel to PSU Airflow	VRF-lite: 64 instances Line-rate layer 2 switching: All protocols, including IPv4 and IPv6 Line-rate layer 3 routing: IPv4 and IPv6 IPv4 host table size: 8K IPv6 host table size: 4K IPv4 multicast table size: 4K LAG load balancing: Based on Layer 2, IPv4 or IPv6 headers Latency: 800ns Packet buffer memory: 9MB CPU memory: 2GB
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Claim 6

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(a) Packets are matched against multiple tables in the pipeline

<https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf>, p. 13.

5.2 Flow Table

A flow table consists of flow entries.

Match Fields	Priority	Counters	Instructions	Timeouts	Cookie
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Table~1: Main components of a flow entry in a flow table.

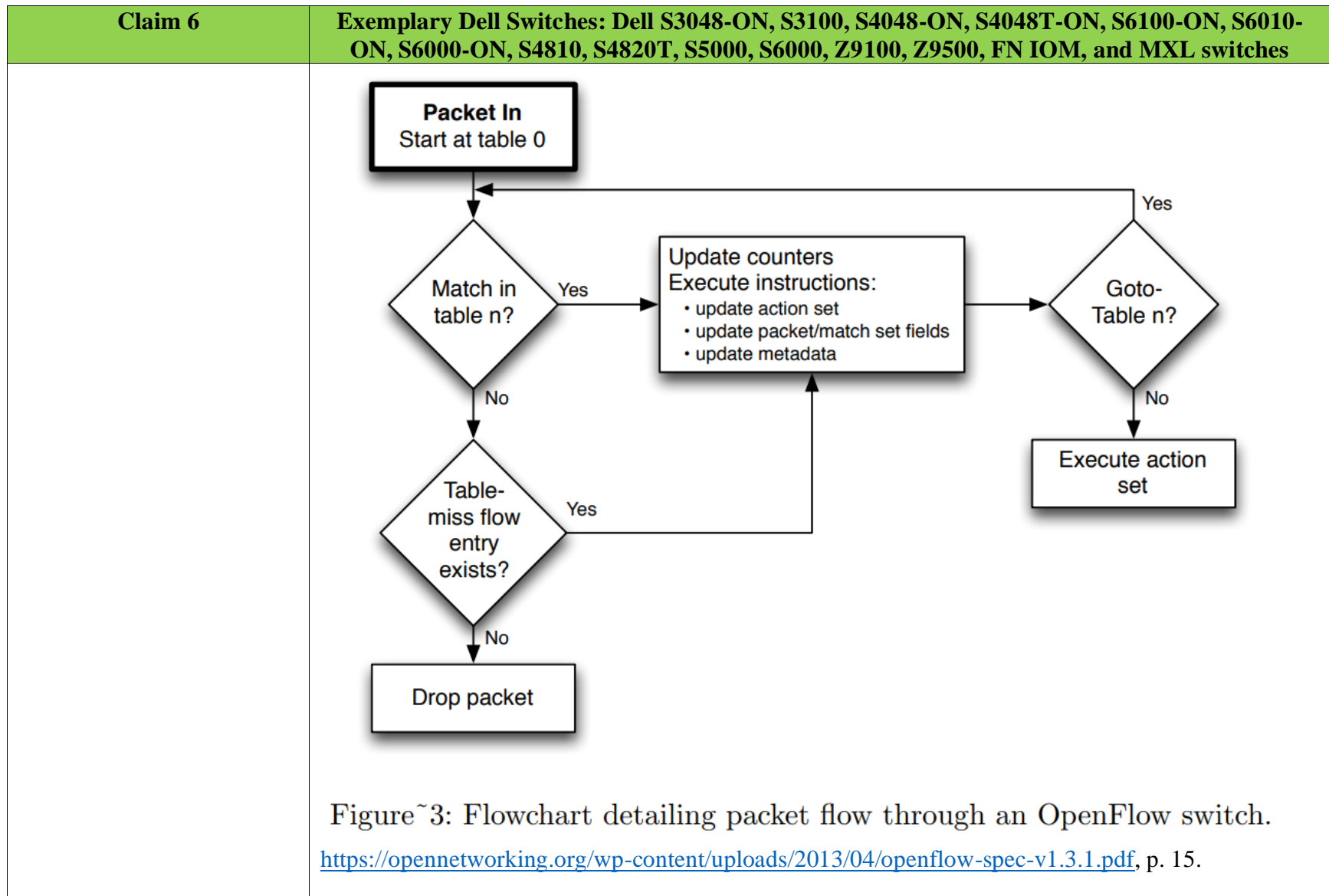
Each flow table entry (see Table 1) contains:

- **match fields:** to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table.

<https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf>, p. 14.

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches
	<p>A.2.3.7 Flow Match Fields</p> <p>The specification defines a default set of match fields with <code>oxm_class=OFPXMC_OPENFLOW_BASIC</code> which can have the following values:</p> <pre> /* OXM Flow match field types for OpenFlow basic class. */ enum oxm_ofb_match_fields { OFPXMT_OFB_IN_PORT = 0, /* Switch input port. */ OFPXMT_OFB_IN_PHY_PORT = 1, /* Switch physical input port. */ OFPXMT_OFB_METADATA = 2, /* Metadata passed between tables. */ OFPXMT_OFB_ETH_DST = 3, /* Ethernet destination address. */ OFPXMT_OFB_ETH_SRC = 4, /* Ethernet source address. */ OFPXMT_OFB_ETH_TYPE = 5, /* Ethernet frame type. */ OFPXMT_OFB_VLAN_VID = 6, /* VLAN id. */ OFPXMT_OFB_VLAN_PCP = 7, /* VLAN priority. */ OFPXMT_OFB_IP_DSCP = 8, /* IP DSCP (6 bits in ToS field). */ OFPXMT_OFB_IP_ECN = 9, /* IP ECN (2 bits in ToS field). */ OFPXMT_OFB_IP_PROTO = 10, /* IP protocol. */ OFPXMT_OFB_IPV4_SRC = 11, /* IPv4 source address. */ OFPXMT_OFB_IPV4_DST = 12, /* IPv4 destination address. */ OFPXMT_OFB_TCP_SRC = 13, /* TCP source port. */ OFPXMT_OFB_TCP_DST = 14, /* TCP destination port. */ OFPXMT_OFB_UDP_SRC = 15, /* UDP source port. */ OFPXMT_OFB_UDP_DST = 16, /* UDP destination port. */ OFPXMT_OFB_SCTP_SRC = 17, /* SCTP source port. */ OFPXMT_OFB_SCTP_DST = 18, /* SCTP destination port. */ } </pre> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 49.</p>
[b] at least one processor configured to execute program instructions to provide a forwarding unit configured to:	<p>The Exemplary Dell Switches have at least one processor configured to execute program instructions.</p> <p><i>See, e.g.,</i></p>

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches				
	<p>Specifications: S4810 high-performance 10/40GbE switch</p> <table border="1"> <thead> <tr> <th data-bbox="611 321 1230 367">Dell SKU description</th><th data-bbox="1255 334 1839 558"></th></tr> </thead> <tbody> <tr> <td data-bbox="611 367 1230 558"> S4810 S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, I/O Panel to PSU Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow, Rear Mnt Bracket S4810, 48x 10GbE SFP+, 4x QSFP+, 1x DC PSU, 2x Fans, I/O Panel to PSU Airflow </td><td data-bbox="1255 334 1839 558"> VRF-lite: 64 instances Line-rate layer 2 switching: All protocols, including IPv4 and IPv6 Line-rate layer 3 routing: IPv4 and IPv6 IPv4 host table size: 8K IPv6 host table size: 4K IPv4 multicast table size: 4K LAG load balancing: Based on Layer 2, IPv4 or IPv6 headers Latency: 800ns Packet buffer memory: 9MB CPU memory: 2GB </td></tr> </tbody> </table> <p>https://i.dell.com/sites/csdocuments/Shared-Content_data-Sheets_Documents/en/Dell_Networking_S4810_Spec_Sheet.pdf</p>	Dell SKU description		S4810 S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, I/O Panel to PSU Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow S4810, 48x 10GbE SFP+, 4x QSFP+, 1x AC PSU, 2x Fans, PSU to I/O Panel Airflow, Rear Mnt Bracket S4810, 48x 10GbE SFP+, 4x QSFP+, 1x DC PSU, 2x Fans, I/O Panel to PSU Airflow	VRF-lite: 64 instances Line-rate layer 2 switching: All protocols, including IPv4 and IPv6 Line-rate layer 3 routing: IPv4 and IPv6 IPv4 host table size: 8K IPv6 host table size: 4K IPv4 multicast table size: 4K LAG load balancing: Based on Layer 2, IPv4 or IPv6 headers Latency: 800ns Packet buffer memory: 9MB CPU memory: 2GB
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[c] receive an incoming packet including a plurality of identifiers,	<p>The Exemplary Dell Switches have a forwarding unit configured to receive an incoming packet including a plurality of the identifiers.</p> <p><i>See, e.g.,</i></p>				



Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches						
	<div>5.1 Pipeline Processing</div> <p>OpenFlow-compliant switches come in two types: <i>OpenFlow-only</i>, and <i>OpenFlow-hybrid</i>. OpenFlow-only switches support only OpenFlow operation, in those switches all packets are processed by the OpenFlow pipeline, and can not be processed otherwise.</p> <p>OpenFlow-hybrid switches support both OpenFlow operation and <i>normal</i> Ethernet switching operation, i.e. traditional L2 Ethernet switching, VLAN isolation, L3 routing (IPv4 routing, IPv6 routing...), ACL and QoS processing. Those switches must provide a classification mechanism outside of OpenFlow that routes traffic to <i>either</i> the OpenFlow pipeline <i>or</i> the normal pipeline. For example, a switch may use the VLAN tag or input port of the packet to decide whether to process the packet using one pipeline or the other, or it may direct all packets to the OpenFlow pipeline. This classification mechanism is outside the scope of this specification. An OpenFlow-hybrid switch may also allow a packet to go from the OpenFlow pipeline to the normal pipeline through the <i>NORMAL</i> and <i>FLOOD</i> reserved ports (see 4.5).</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 13.</p> <div>5.2 Flow Table</div> <p>A flow table consists of flow entries.</p> <table><tr><td>Match Fields</td><td>Priority</td><td>Counters</td><td>Instructions</td><td>Timeouts</td><td>Cookie</td></tr></table> <p>Table~1: Main components of a flow entry in a flow table.</p> <p>Each flow table entry (see Table 1) contains:</p> <ul style="list-style-type: none">• match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 14.</p>	Match Fields	Priority	Counters	Instructions	Timeouts	Cookie
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[d] determine a direction to forward the incoming packet,	<p>The Exemplary Dell Switches have a forwarding unit configured to determine a direction to forward the incoming packet.</p> <p><i>See, e.g.,</i></p>

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches						
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	<p>5.11 Action List</p> <p>The <i>Apply-Actions</i> instruction and the <i>Packet-out</i> message include an action list. The semantics of the action list is identical to the OpenFlow 1.0 specification. The actions of an action list are executed in the order specified by the list, and are applied immediately to the packet.</p> <p>The execution of an action list starts with the first action in the list and each action is executed on the packet in sequence. The effect of those actions is cumulative, if the action list contains two Push VLAN actions, two VLAN headers are added to the packet. If the action list contains an output action, a copy of the packet is forwarded in its current state to the desired port. If the list contains group actions, a copy of the packet in its current state is processed by the relevant group buckets.</p> <p>After the execution of the action list in an <i>Apply-Actions</i> instruction, pipeline execution continues on the modified packet (see 5.1). The action set of the packet is unchanged by the execution of the action list.</p> <p>5.12 Actions</p> <p>A switch is not required to support all action types, just those marked “<i>Required Action</i>” below. The controller can also query the switch about which of the “<i>Optional Action</i>” it supports.</p> <p><i>Required Action: Output.</i> The Output action forwards a packet to a specified OpenFlow port (see 4.1). OpenFlow switches must support forwarding to physical ports, switch-defined logical ports and the required reserved ports (see 4.5).</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 22.</p>

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches
	<p>An <i>Output</i> action uses the following structure and fields:</p> <pre> /* Action structure for OFPAT_OUTPUT, which sends packets out 'port'. * When the 'port' is the OFPP_CONTROLLER, 'max_len' indicates the max * number of bytes to send. A 'max_len' of zero means no bytes of the * packet should be sent. A 'max_len' of OFPCML_NO_BUFFER means that * the packet is not buffered and the complete packet is to be sent to * the controller. */ struct ofp_action_output { uint16_t type; /* OFPAT_OUTPUT. */ uint16_t len; /* Length is 16. */ uint32_t port; /* Output port. */ uint16_t max_len; /* Max length to send to controller. */ uint8_t pad[6]; /* Pad to 64 bits. */ }; OFP_ASSERT(sizeof(struct ofp_action_output) == 16); </pre> <p>The <code>port</code> specifies the port through which the packet should be sent. The <code>max_len</code> indicates the maximum amount of data from a packet that should be sent when the port is <code>OFPP_CONTROLLER</code>. If <code>max_len</code> is zero, the switch must send zero bytes of the packet. A <code>max_len</code> of <code>OFPCML_NO_BUFFER</code> means that the complete packet should be sent, and it should not be buffered.</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 57.</p>
[e] determine whether to forward to an external network,	<p>The Exemplary Dell Switches have a forwarding unit configured to determine whether to forward to an external network.</p> <p><i>See, e.g.,</i></p>

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches						
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	<p>5.11 Action List</p> <p>The <i>Apply-Actions</i> instruction and the <i>Packet-out</i> message include an action list. The semantics of the action list is identical to the OpenFlow 1.0 specification. The actions of an action list are executed in the order specified by the list, and are applied immediately to the packet.</p> <p>The execution of an action list starts with the first action in the list and each action is executed on the packet in sequence. The effect of those actions is cumulative, if the action list contains two Push VLAN actions, two VLAN headers are added to the packet. If the action list contains an output action, a copy of the packet is forwarded in its current state to the desired port. If the list contains group actions, a copy of the packet in its current state is processed by the relevant group buckets.</p> <p>After the execution of the action list in an <i>Apply-Actions</i> instruction, pipeline execution continues on the modified packet (see 5.1). The action set of the packet is unchanged by the execution of the action list.</p> <p>5.12 Actions</p> <p>A switch is not required to support all action types, just those marked “<i>Required Action</i>” below. The controller can also query the switch about which of the “<i>Optional Action</i>” it supports.</p> <p><i>Required Action: Output.</i> The Output action forwards a packet to a specified OpenFlow port (see 4.1). OpenFlow switches must support forwarding to physical ports, switch-defined logical ports and the required reserved ports (see 4.5).</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 22.</p>

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	<p>An <i>Output</i> action uses the following structure and fields:</p> <pre> /* Action structure for OFPAT_OUTPUT, which sends packets out 'port'. * When the 'port' is the OFPP_CONTROLLER, 'max_len' indicates the max * number of bytes to send. A 'max_len' of zero means no bytes of the * packet should be sent. A 'max_len' of OFPCML_NO_BUFFER means that * the packet is not buffered and the complete packet is to be sent to * the controller. */ struct ofp_action_output { uint16_t type; /* OFPAT_OUTPUT. */ uint16_t len; /* Length is 16. */ uint32_t port; /* Output port. */ uint16_t max_len; /* Max length to send to controller. */ uint8_t pad[6]; /* Pad to 64 bits. */ }; OFP_ASSERT(sizeof(struct ofp_action_output) == 16); </pre> <p>The <code>port</code> specifies the port through which the packet should be sent. The <code>max_len</code> indicates the maximum amount of data from a packet that should be sent when the port is <code>OFPP_CONTROLLER</code>. If <code>max_len</code> is zero, the switch must send zero bytes of the packet. A <code>max_len</code> of <code>OFPCML_NO_BUFFER</code> means that the complete packet should be sent, and it should not be buffered.</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 57.</p>
[f] add to path or link information when the incoming packet is not forwarded to the external network,	<p>The Exemplary Dell Switches have a forwarding unit configured to add to path or link information when the incoming packet is not forwarded to the external network.</p> <p><i>See, e.g.,</i></p>

Claim 6	Exemplary Dell Switches: Dell S3048-ON, S3100, S4048-ON, S4048T-ON, S6100-ON, S6010-ON, S6000-ON, S4810, S4820T, S5000, S6000, Z9100, Z9500, FN IOM, and MXL switches						
	<div>5.2 Flow Table</div> <div>A flow table consists of flow entries.</div> <div><table><tr><td>Match Fields</td><td>Priority</td><td>Counters</td><td>Instructions</td><td>Timeouts</td><td>Cookie</td></tr></table></div> <div>Table~1: Main components of a flow entry in a flow table.</div> <div>Each flow table entry (see Table 1) contains:</div> <div><ul style="list-style-type: none">• match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table.</div> <div>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 14.</div> <div>5.9 Instructions</div> <div>Each flow entry contains a set of instructions that are executed when a packet matches the entry. These instructions result in changes to the packet, action set and/or pipeline processing.</div> <div>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 19.</div>	Match Fields	Priority	Counters	Instructions	Timeouts	Cookie
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	<p>5.11 Action List</p> <p>The <i>Apply-Actions</i> instruction and the <i>Packet-out</i> message include an action list. The semantics of the action list is identical to the OpenFlow 1.0 specification. The actions of an action list are executed in the order specified by the list, and are applied immediately to the packet.</p> <p>The execution of an action list starts with the first action in the list and each action is executed on the packet in sequence. The effect of those actions is cumulative, if the action list contains two Push VLAN actions, two VLAN headers are added to the packet. If the action list contains an output action, a copy of the packet is forwarded in its current state to the desired port. If the list contains group actions, a copy of the packet in its current state is processed by the relevant group buckets.</p> <p>After the execution of the action list in an <i>Apply-Actions</i> instruction, pipeline execution continues on the modified packet (see 5.1). The action set of the packet is unchanged by the execution of the action list.</p> <p>5.12 Actions</p> <p>A switch is not required to support all action types, just those marked “<i>Required Action</i>” below. The controller can also query the switch about which of the “<i>Optional Action</i>” it supports.</p> <p><i>Required Action: Output.</i> The Output action forwards a packet to a specified OpenFlow port (see 4.1). OpenFlow switches must support forwarding to physical ports, switch-defined logical ports and the required reserved ports (see 4.5).</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 22.</p>

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	<p><i>Optional Action: Push-Tag/Pop-Tag.</i> Switches may support the ability to push/pop tags as shown in Table 6. To aid integration with existing networks, we suggest that the ability to push/pop VLAN tags be supported.</p> <p>Newly pushed tags should <i>always</i> be inserted as the outermost tag in the outermost valid location for that tag. When a new VLAN tag is pushed, it should be the outermost tag inserted, immediately after the Ethernet header and before other tags. Likewise, when a new MPLS tag is pushed, it should be the outermost tag inserted, immediately after the Ethernet header and before other tags.</p> <p>When multiple push actions are added to the action set of the packet, they apply to the packet in the order defined by the action set rules, first MPLS, then PBB, than VLAN (se 5.10). When multiple push actions are included in an action list, they apply to the packet in the list order (see 5.11).</p> <table><tr><th>Action</th><th>Associated Data</th><th>Description</th></tr><tr><td>Push VLAN header</td><td>Ethertype</td><td>Push a new VLAN header onto the packet. The Ethertype is used as the Ethertype for the tag. Only Ethertype 0x8100 and 0x88a8 should be used.</td></tr><tr><td>Pop VLAN header</td><td>-</td><td>Pop the outer-most VLAN header from the packet.</td></tr><tr><td>Push MPLS header</td><td>Ethertype</td><td>Push a new MPLS shim header onto the packet. The Ethertype is used as the Ethertype for the tag. Only Ethertype 0x8847 and 0x8848 should be used.</td></tr><tr><td>Pop MPLS header</td><td>Ethertype</td><td>Pop the outer-most MPLS tag or shim header from the packet. The Ethertype is used as the Ethertype for the resulting packet (Ethertype for the MPLS payload).</td></tr><tr><td>Push PBB header</td><td>Ethertype</td><td>Push a new PBB service instance header (I-TAG TCI) onto the packet (see A.2.5). The Ethertype is used as the Ethertype for the tag. Only Ethertype 0x88E7 should be used.</td></tr><tr><td>Pop PBB header</td><td>-</td><td>Pop the outer-most PBB service instance header (I-TAG TCI) from the packet (see A.2.5).</td></tr></table> <p>Table~6: Push/pop tag actions.</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 23.</p>	Action	Associated Data	Description	Push VLAN header	Ethertype	Push a new VLAN header onto the packet. The Ethertype is used as the Ethertype for the tag. Only Ethertype 0x8100 and 0x88a8 should be used.	Pop VLAN header	-	Pop the outer-most VLAN header from the packet.	Push MPLS header	Ethertype	Push a new MPLS shim header onto the packet. The Ethertype is used as the Ethertype for the tag. Only Ethertype 0x8847 and 0x8848 should be used.	Pop MPLS header	Ethertype	Pop the outer-most MPLS tag or shim header from the packet. The Ethertype is used as the Ethertype for the resulting packet (Ethertype for the MPLS payload).	Push PBB header	Ethertype	Push a new PBB service instance header (I-TAG TCI) onto the packet (see A.2.5). The Ethertype is used as the Ethertype for the tag. Only Ethertype 0x88E7 should be used.	Pop PBB header	-	Pop the outer-most PBB service instance header (I-TAG TCI) from the packet (see A.2.5).
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[g] remove path or link information header when the incoming packet is forwarded to the external network, and	<p>The Exemplary Dell Switches have a forwarding unit configured to remove path or link information header when the incoming packet is forwarded to the external network.</p> <p><i>See, e.g.,</i></p> <h2>VLAN Tag Removal</h2> <p>This feature allows an interface processor (IFP) action to remove the outer VLAN tag from a packet before sending it out of the egress port. OpenFlow VLAN egress ports are now supported and flows with the <code>strip-vlan</code> action and an OF VLAN member port as the egress port are accepted.</p> <p>① NOTE: This feature is supported for OF egress ports only.</p> <p>Dell OpenFlow Deployment and User Guide 4.0, p. 25.</p> <h3>5.2 Flow Table</h3> <p>A flow table consists of flow entries.</p> <table><tr><td>Match Fields</td><td>Priority</td><td>Counters</td><td>Instructions</td><td>Timeouts</td><td>Cookie</td></tr></table> <p>Table~1: Main components of a flow entry in a flow table.</p> <p>Each flow table entry (see Table 1) contains:</p> <ul style="list-style-type: none">• match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 14.</p> <h3>5.9 Instructions</h3> <p>Each flow entry contains a set of instructions that are executed when a packet matches the entry. These instructions result in changes to the packet, action set and/or pipeline processing.</p>	Match Fields	Priority	Counters	Instructions	Timeouts	Cookie
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	<p data-bbox="604 349 835 378">5.11 Action List</p> <p data-bbox="604 422 1848 516">The <i>Apply-Actions</i> instruction and the <i>Packet-out</i> message include an action list. The semantics of the action list is identical to the OpenFlow 1.0 specification. The actions of an action list are executed in the order specified by the list, and are applied immediately to the packet.</p> <p data-bbox="604 540 1848 706">The execution of an action list starts with the first action in the list and each action is executed on the packet in sequence. The effect of those actions is cumulative, if the action list contains two Push VLAN actions, two VLAN headers are added to the packet. If the action list contains an output action, a copy of the packet is forwarded in its current state to the desired port. If the list contains group actions, a copy of the packet in its current state is processed by the relevant group buckets.</p> <p data-bbox="604 730 1848 824">After the execution of the action list in an <i>Apply-Actions</i> instruction, pipeline execution continues on the modified packet (see 5.1). The action set of the packet is unchanged by the execution of the action list.</p> <p data-bbox="604 885 787 914">5.12 Actions</p> <p data-bbox="604 958 1848 1019">A switch is not required to support all action types, just those marked “<i>Required Action</i>” below. The controller can also query the switch about which of the “<i>Optional Action</i>” it supports.</p> <p data-bbox="604 1044 1848 1138"><i>Required Action: Output.</i> The Output action forwards a packet to a specified OpenFlow port (see 4.1). OpenFlow switches must support forwarding to physical ports, switch-defined logical ports and the required reserved ports (see 4.5).</p> <p data-bbox="604 1157 1738 1187">https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 22.</p>

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[h] to forward the incoming packet by using a path or link that corresponds to the identifier from among the plurality of identifiers.	<p>The Exemplary Dell Switches have a forwarding unit configured to forward the incoming packet by using a path or link that corresponds to the identifier from among the plurality of identifiers.</p> <p><i>See, e.g.,</i></p> <p>5.2 Flow Table</p> <p>A flow table consists of flow entries.</p> <table><tr><td>Match Fields</td><td>Priority</td><td>Counters</td><td>Instructions</td><td>Timeouts</td><td>Cookie</td></tr></table> <p>Table~1: Main components of a flow entry in a flow table.</p> <p>Each flow table entry (see Table 1) contains:</p> <ul style="list-style-type: none">• match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 14.</p> <p>5.9 Instructions</p> <p>Each flow entry contains a set of instructions that are executed when a packet matches the entry. These instructions result in changes to the packet, action set and/or pipeline processing.</p> <p>https://opennetworking.org/wp-content/uploads/2013/04/openflow-spec-v1.3.1.pdf, p. 19.</p>	Match Fields	Priority	Counters	Instructions	Timeouts	Cookie
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